

曜凌光電股份有限公司

住址: 42878 台中市大雅区科雅路 25 號 5F WEB: http://www.Raystar-Optronics.com
5F., No.25, Keya Rd., Daya Dist., Taichung E-mail: sales@raystar-optronics.com
City 428, Taiwan
Tel:886-4-2565-0761 Fax: 886-4-2565-0760

RG12864A-BIY-V

SPECIFICATION

CUSTOMER:

APPROVED BY	
PCB VERSION	
DATE	

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

ISSUED DATE:



Contents

		Page
1.	Revision History	3
2.	General Specification	4
3.	Module Coding System	5
4.	Interface Pin Function	6
5.	Outline dimension & Block Diagram	7
6.	Display Control Instruction	8
7.	Timing Characteristics	11
8.	Optical Characteristics	12
9.	Absolute Maximum Ratings	13
10.	Electrical Characteristics	13
11.	Backlight Information	14
12.	Reliability	15
13.	Inspection specification	16
14.	Precautions in use of LCD Modules	20
15.	Material List of Components for RoHs	21
16.	Recommendable storage	21



1. Revision History

DATE	VERSION	REVISED PAGE NO.	Note
2012/08/13	1		First issue



2. General Specification

The Features is described as follow:

■ Module dimension: 93.0 x 70.0 x 13.6 (max.) mm³

View area: 72.0 x 40.0 mm²

Active area: 66.52 x 33.24 mm²

■ Number of dots: 128 x 64

■ Dot size: 0.48 x 0.48 mm²

■ Dot pitch: 0.52 x 0.52 mm²

■ LCD type: STN Negative, Blue Transmissive

■ Duty: 1/64

■ View direction: 6 o'clock

■ Backlight Type: LED, Yellow Green



3. Module Coding System

R	G	12864	Α	-	В	I	Υ	-	٧
1	2	3	4	-	5	6	7	-	8

Item		Description	on				
1	R: Raystar C	optronics Inc.					
2	Display	C: Character Type,					
	Display	G: Graphic Type					
3		s: 128 x64 Dots					
4	Serials code	_					
		P : TN Positive, Gray					
		N: TN Negative,					
		G: STN Positive, Gray					
5	LCD	Y: STN Positive, Yellow Gr	een				
		B: STN Negative, Blue					
		F: FSTN Positive					
		T: FSTN Negative					
	Polarizer Type,	A: Reflective, N.T, 6:00	K: Transflective, W.T,12:00				
		D: Reflective, N.T, 12:00	1: Transflective, U.T,6:00				
		G: Reflective, W. T, 6:00	4: Transflective, U.T.12:00				
	_ ,	J: Reflective, W. T, 12:00	C: Transmissive, N.T,6:00				
6	Temperature range,	0: Reflective, U. T, 6:00	F: Transmissive, N.T,12:00				
	range,	3: Reflective, U. T, 12:00	I: Transmissive, W. T, 6:00				
	View	B: Transflective, N.T,6:00	L: Transmissive, W.T,12:00				
	direction	E: Transflective, N.T.12:00	2: Transmissive, U. T, 6:00				
		H: Transflective, W.T,6:00	5 : Transmissive, U.T,12:00				
		N: Without backlight	Y: LED, Yellow Green				
		P: EL, Blue green	A: LED, Amber				
7	Backlight	T: EL, Green	W: LED, White				
		D: EL, White	O: LED, Orange				
		F: CCFL, White	G: LED, Green				
8	Special code	V: Build-in Negative Voltage					

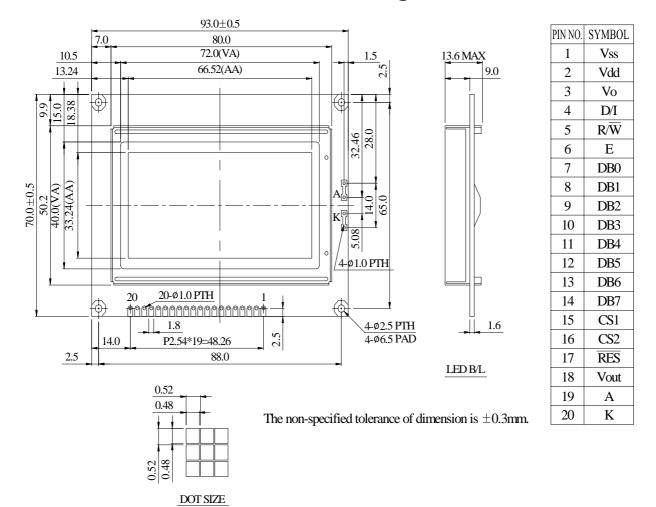


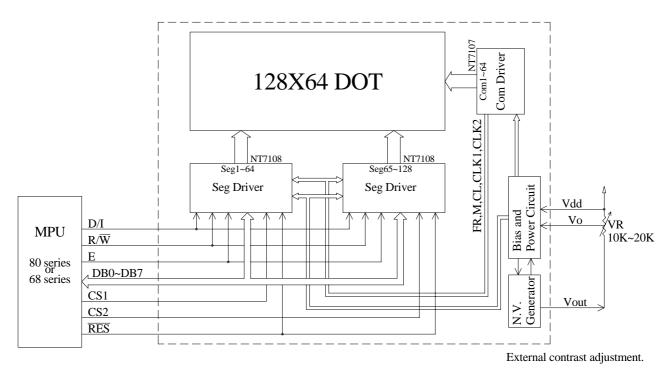
4. Interface Pin Function

Pin#	Symbol	Level	Description
1	Vss	0V	Ground
2	V _{DD}	5.0V	Supply voltage for logic
3	V _o	(Variable)	Operating voltage for LCD
4	D/I	H/L	H: Data, L: Instruction
5	R/W	H/L	H: Read (MPU←Module) , L: Write (MPU→Module)
6	E	Н	Enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	CS1	Н	Select Column 1~ Column 64
16	CS2	Н	Select Column 65~ Column 128
17	RES	L	Reset signal
18	Vout	_	Negative Voltage Output
19	A	_	Power Supply for LED backlight (+)
20	K	_	Power Supply for LED backlight (-)



5. Outline Dimension & Block Diagram







6. Display Control Instruction

The internal state of NT7108 is defined by Display Control Instruction, sent by MPU, shown in the table below.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function		
Display on/off	L	L	L	L	Н	Н	Н	H H L/H			Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON		
Set address (Y address)	L	L	L	Н		Y address (0-63)					Sets the Y address in the Y address counter.		
Set page (X address)	L	L	Н	L	Н	H H Page (0-7)				-7)	Sets the X address at the X address register.		
Display Start line (Z address)	L	L	Н	Н		Display start line (0-63)					Indicates the display data RAM displayed at the top of the screen.		
Status read	L H		Busy	L	On/ Off	Reset	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset		
Write display data	Н	L									Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.		
Read display data	Н	Н				Read data					Reads data (DB0: 7) from display data RAM to the data bus.		

Instruction

Display ON/OFF

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

D gives the value of DB0. The Display Data appears as D is 1, and disappears as D is 0. The Display Data still remains in Display Data RAM as D is 0, though the data is not shown on the screen. The Display Data can reappear by switching D from 0 to 1.



SET ADDRESS (Y ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

The Y addresses (AC0-AC5) of Display Data RAM are set in the Y address counter. Each address is set by instruction and incremented by 1 automatically by read or write operations.

SET PAGE (X ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

The X address (AC0-AC2) of Display Data RAM is set in the X address counter. Writing or reading to or from MPU is executed in this specified page until the next page is set.

DISPLAY START LINE (Z ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z addresses (AC0-AC5) of the Display Data RAM are set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen are displayed from the line specified by display start line instruction.

STATUS READ

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

As BUSY is 1, the Chip is executing internal operation and can not accept any instruction.

As BUSY is 0, the Chip is ready to accept any instruction.

ON/OFF

As ON/OFF is 1, the display is OFF.

As ON/OFF is 0, the display is ON.

RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions can be accepted except for the Status Read instruction.

As RESET is 0, initializing has finished and the system is in its normal operation condition.



WRITE DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

The Write Display Data (D0-D7) are written into the Display Data RAM. Completing the writing instruction, Y address is increased by 1 automatically.

READ DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

The Read Display Data (D0-D7) are read from the display data RAM. Completing the reading instruction, Y address is increased by 1 automatically.

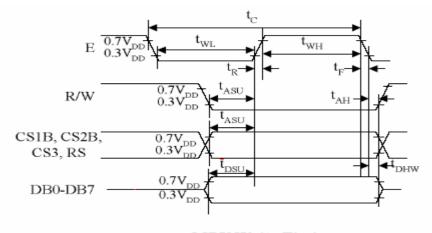


7. Timing Characteristics

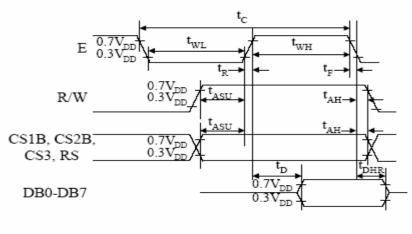
MPU Interface

(T=25°C, VDD=+5.0V±0.5)

Characteristic	Symbol	Min	Тур	Max	Unit
E cycle	tcyc	1000	_	_	ns
E high level width	twhE	450	_	_	ns
E low level width	twlE	450	_	_	ns
E rise time	tr	_	_	25	ns
E tall time	tf	_	_	25	ns
Address set-up time	tas	140	_	_	ns
Address hold time	tah	10	_	_	ns
Data set-up time	tdsw	140	_	_	ns
Data delay time	tddr	_	_	320	ns
Data hold time (write)	tdhw	10	_	_	ns
Data hold time (read)	tdhr	20	_	_	ns



MPU Write Timing



MPU Read Timing

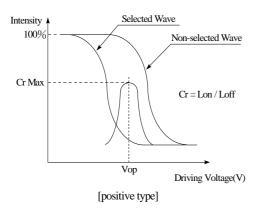


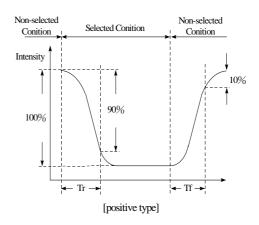
8. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	CR≧2	20	_	40	deg
View 7 tilgie	(Η)φ	CR≧2	-30	_	30	deg
Contrast Ratio	CR	_	_	3	_	_
Response Time	T rise	_	_	200	300	ms
	T fall	_	_	200	300	ms

Definition of Operation Voltage, Vop.

Definition of Response Time, Tr and Tf.



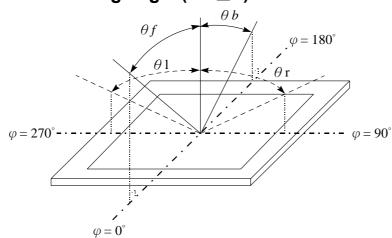


Conditions:

Operating Voltage : Vop Viewing Angle(θ , ϕ) : 0° , 0°

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

Definition of viewing angle (CR≥2)





9. Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	T _{OP}	-20	_	+70	$^{\circ}\!\mathbb{C}$
Storage Temperature	T _{ST}	-30	_	+80	$^{\circ}\!\mathbb{C}$
Input Voltage	Vı	0	_	V_{DD}	V
Supply Voltage For Logic	V_{DD}	0	_	6.7	V
Supply Voltage For LCD	V _{DD} -V _{LCD}	0	_	16.7	V
Supply Voltage For LCD	VDD-V _{OUT}	_	_	-10	V

10. Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V_{DD} - V_{SS}	_	4.5	5.0	5.5	V
		Ta=-20°C	_	_	10.6	V
Supply Voltage For LCD	V_{DD} - V_0	Ta=25℃	8.6	8.9	9.2	V
		Ta=+70℃	7.6	_	_	V
Input High Volt.	V _{IH}	_	2.0	_	V_{DD}	V
Input Low Volt.	V_{IL}	_	0	_	0.8	V
Output High Volt.	V _{OH}	_	2.4	_	V_{DD}	V
Output Low Volt.	V_{OL}	_	_	_	0.4	V
Supply Current	I _{DD}	_	_	18	_	mA



11. Backlight Information

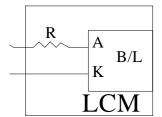
Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	I _{LED}	297	330	396	mA	V=4.1V
Supply Voltage	V	4.0	4.1	4.3	V	_
Reverse Voltage	VR	_	_	8	V	_
Luminous Intensity	IV	220	280	_	cd/m²	I _{LED} =330mA
Wave Length	λр	568	571	573	nm	I _{LED} =330mA
Life Time	_	_	100000	_	hr.	V≦4.1V
Color	Yellow Gr	een	1	I	1	

Note:

The LED of B/L is drive by current only; drive voltage is for reference only. Drive voltage has to make driving current under safety area.(The current between minimum and maximum)

.Drive from pin19,pin20





12. Reliability

Content of Reliability Test (wide temperature, -20°c~70°C)

Environmental Test									
Test Item	Content of Test	Condition	Note						
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2						
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	200hrs	1,2						
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	200hrs	-						
Low Temperature Operation	temperature for a long time.	-20℃ 200hrs	1						
High Temperature/ Humidity Operation	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2						
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	-						
Vibration test	Endurance test applying the vibration during transportation and using.	fixed amplitude: 15mm Vibration. Frequency: 10~55Hz. One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3						
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS= 1.5kΩ CS=100pF 1 time							

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.



13. Inspection specification

NO	Item			Criterion		AQL		
01	Electrical Testing	1.3 Display mal 1.4 No function 1.5 Current con 1.6 LCD viewin 1.7 Mixed produ	defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.					
02	Black or white spots on LCD (display only)	than three v	 .1 White and black spots on display ≤0.25mm, no more than three white or black spots present. .2 Densely spaced: No more than two spots or lines within 3mm 					
03	LCD black spots, white spots, contaminatio	3.1 Round type Φ=(x + y)		wing drawing		2.5		
	n (non-display)	3.2 Line type :	$ \begin{array}{c c} \text{(As follow} \\ \hline \text{Length} \\ \hline & \\ \hline & \text{L} \leqq 3.0 \\ \hline & \text{L} \leqq 2.5 \\ \hline & \\ \hline \end{array} $	ring drawing) Width W≦0.02 0.02 <w≦0.03 0.03<w≦0.05="" 0.05<w<="" td=""><td>Acceptable Q TY Accept no dense 2 As round type</td><td>2.5</td></w≦0.03>	Acceptable Q TY Accept no dense 2 As round type	2.5		
04	Polarizer bubbles	If bubbles are vigudge using blaspecifications, easy to find, mocheck in specification.	ack spot not ust	Size Φ $ Φ \le 0.20 $ $ 0.20 < Φ \le 0.50 $ $ 0.50 < Φ \le 1.00 $ $ 1.00 < Φ $ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5		



NO	Item		Criterion		AQL
05	Scratches	Follow NO.3 LCD blac		contamination	7.5
06	Chipped glass	Symbols Define: x: Chip length k: Seal width L: Electrode pad lengt 6.1 General glass chip 6.1.1 Chip on panel su z : Chip thickness $z \le 1/2t$ $1/2t < z \le 2t$ 0 If there are 2 or more 6.1.2 Corner crack: $z \le 1/2t$ $1/2t < z \le 2t$ $z \le 1/2t$	y: Chip width Not over viewing area Not exceed 1/3k re chips, x is total lenger area Not over viewing area Not over viewing area Not exceed 1/3k re chips, x is total lenger area Not over viewing area Not over viewing area	hip thickness LCD side length veen panels: $x: Chip length$ $x \le 1/8a$	2.5



NO	Item	Criterion	AQL
NO 06	Item	$Symbols: \\ x: Chip length & y: Chip width & z: Chip thickness \\ k: Seal width & t: Glass thickness & a: LCD side length \\ L: Electrode pad length \\ 6.2 Protrusion over terminal: \\ 6.2.1 Chip on electrode pad: \\ \hline \\ y: Chip width & x: Chip length & z: Chip thickness \\ y \leq 0.5 mm & x \leq 1/8a & 0 < z \leq t \\ \hline \\ 6.2.2 \ Non-conductive portion: \\ \hline \\ \\ L$	AQL
		y: Chip width x: Chip length z: Chip thickness	
		$y \le L$ $x \le 1/8a$ $0 < z \le t$	
		 Olf the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. Olf the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack. y: width x: length y≤1/3L x≤ a 	



NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	 8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong. 	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.9.2 Bezel must comply with job specifications.	
10	PCB · COB	 10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB X * Y<=2mm²	2.5 2.5 0.65 2.5 2.5 0.65 2.5 2.5
11	Soldering	 11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB. 	2.5 2.5 2.5 0.65



NO	Item	Criterion	AQL
12	General appearance	 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. 12.2 No cracks on interface pin (OLB) of TCP. 12.3 No contamination, solder residue or solder balls on product. 12.4 The IC on the TCP may not be damaged, circuits. 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever. 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. 12.7 Sealant on top of the ITO circuit has not hardened. 12.8 Pin type must match type in specification sheet. 12.9 LCD pin loose or missing pins. 12.10 Product packaging must the same as specified on packaging specification sheet. 12.11 Product dimension and structure must conform to product specification sheet. 	2.5 0.65 2.5 2.5 2.5 2.5 0.65 0.65 0.65

14. Precautions in use of LCD Modules

- 1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- 2. Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- 3. Don't disassemble the LCM.
- 4. Don't operate it above the absolute maximum rating.
- 5. Don't drop, bend or twist LCM.
- 6. Soldering: only to the I/O terminals.
- 7. Storage: please storage in anti-static electricity container and clean environment.
- 8. Raystar have the right to change the passive components (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- 9. Raystar have the right to change the PCB Rev.



15. Material List of Components for RoHs

1. RAYSTAR Optronics Co., Ltd. hereby declares that all of or part of products, including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

- 2. Process for RoHS requirement:
 - (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
 - (2) Heat-resistance temp. :

Reflow: 250°C, 30 seconds Max.;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : $235\pm5^{\circ}$ C;

Recommended customer's soldering temp. of connector: 280°C, 3 seconds.

16. Recommendable storage

- 1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
- 2. Do not place the module near organics solvents or corrosive gases.
- 3. Do not crush, shake, or jolt the module



Page: 1

		i age.			
LCM Sample Estimate Feedback Sheet					
Module Number :					
1 · Panel Specification :					
1. Panel Type:	□ Pass	□ NG ,			
2. View Direction:	□ Pass	□ NG ,			
3. Numbers of Dots:	□ Pass	□ NG ,			
4. View Area:	□ Pass	□ NG ,			
5. Active Area:	□ Pass	□ NG ,			
6.Operating	□ Pass	□ NG ,			
Temperature :					
7.Storage Temperature:	□ Pass	□ NG ,			
8.Others:					
2 · Mechanical Specification	<u>on</u> :				
1. PCB Size:	□ Pass	□ NG ,			
2.Frame Size :	□ Pass	□ NG ,			
3.Materal of Frame:	□ Pass	□ NG ,			
4.Connector Position:	□ Pass	□ NG ,			
5.Fix Hole Position:	□ Pass	□ NG ,			
6.Backlight Position:	□ Pass	□ NG ,			
7. Thickness of PCB:	□ Pass	□ NG ,			
8. Height of Frame to	□ Pass	□ NG ,			
PCB:					
9.Height of Module:	□ Pass	□ NG ,			
10.Others:	□ Pass	□ NG ,			
3 · Relative Hole Size :					
1.Pitch of Connector:	□ Pass	□ NG ,			
2.Hole size of	□ Pass	□ NG ,			
Connector:					
3.Mounting Hole size:	□ Pass	□ NG ,			
4.Mounting Hole Type:	□ Pass	□ NG ,			
5.Others:	□ Pass	□ NG ,			
4 · Backlight Specification :					
1.B/L Type:	□ Pass	□ NG ,			
2.B/L Color:	□ Pass	□ NG ,			
3.B/L Driving Voltage (Reference for LED Type) : □ Pass □ NG ,					
4.B/L Driving Current:	□ Pass	□ NG ,			
5.Brightness of B/L:	□ Pass	□ NG ,			
6.B/L Solder Method:	□ Pass	□ NG ,			
7.Others:	□ Pass	□ NG ,			
	l				

>> Go to page 2 <<





Page: 2

Module Number :				
5 · Electronic Characteristics of Module :				
1.Input Voltage :	□ Pass	□ NG ,		
2.Supply Current:	□ Pass	□ NG ,		
3.Driving Voltage for LCD:	□ Pass	□ NG ,		
4.Contrast for LCD:	□ Pass	□ NG ,		
5.B/L Driving Method:	□ Pass	□ NG ,		
6.Negative Voltage	□ Pass	□ NG ,		
Output:				
7.Interface Function:	□ Pass	□ NG ,		
8.LCD Uniformity:	□ Pass	□ NG ,		
9.ESD test:	□ Pass	□ NG ,		
10.Others:	□ Pass	□ NG ,		
6 · Summary :				
6 · Summary:				
Sales signature :				
Customer Signature: <u>Date: / /</u>				